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PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:

Hideyuki Andoh

Serial No. 10/013,087

Filed: December 7, 2001

For: SEMICONDUCTOR DEVICES AND THE
MANUFACTURING METHOD OF THE SAME

Assistant Commissioner for Patents
Washington, D.C. 20231
Sir:

Transmitted herewith is a paper in the above-identified application.

- Small entity status of this application under 37 C.F.R. §1.9 and §1.27 has been established by a verified statement.
- Applicant claims small entity status.
- No additional fee is required.
- The additional fee is calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE	RATE	ADDITIONAL FEE
TOTAL	11	MINUS	20=	0	x9	\$	x18	\$-0-
INDEP.	2	MINUS	3=	0	x42	\$	x84	\$-0-
FIRST PRESENTATION OF					+140	\$	+280	\$
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			TOTAL ADDITIONAL FEE			\$	\$-0-	

- A check in the amount of \$ is attached.
- The Commissioner is hereby authorized to charge any additional fees required under 37 CFR §1.16, or credit any overpayment, to Account No. 11-0855. A duplicate copy of this sheet is enclosed.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on December 2, 2002.

Signature

KILPATRICK STOCKTON LLP
1100 Peachtree Street
Suite 2800
Atlanta, Georgia 30309-4530
Telephone: 404/815-6500

By:
Roger T. Frost, Attorney for Applicant
Reg. No. 22,176